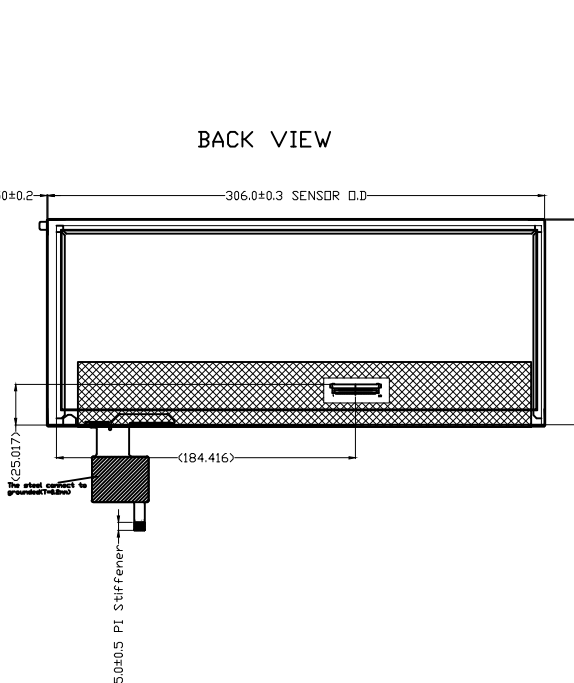
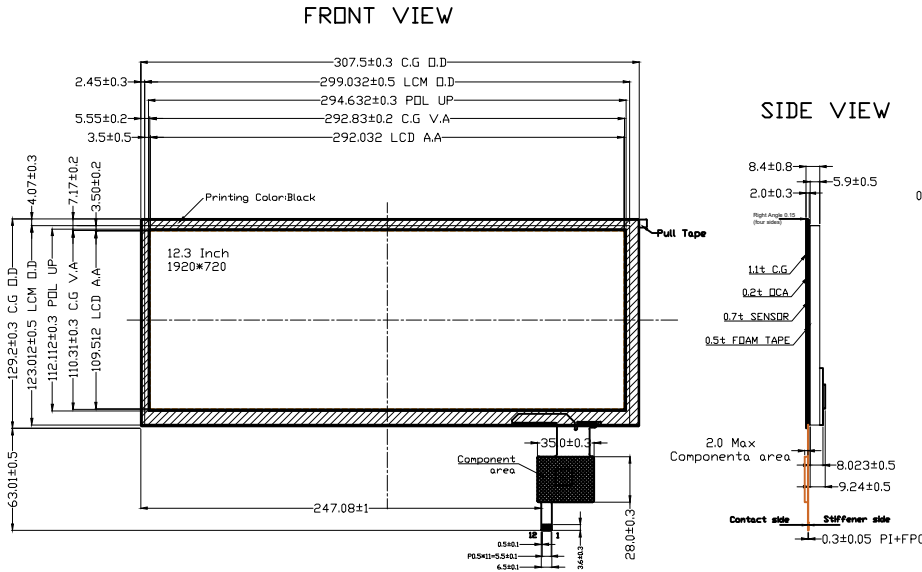


K	L	M	N
REV	DC/EC NUMBER	DESCRIPTION	DATE

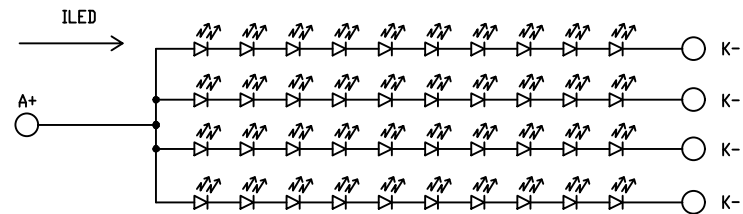


CTP FPC Logic		TFT INTERFACE	
PIN	Logic		
1	PSEL(NC)	1	GND
2	VBUS	2	BIST
3	DN	3	VCC
4	DP	4	VCC
5	GND	5	GND
6	IOVCC(NC)	6	GND
7	VDD	7	OTP
8	SCL	8	NC
9	SDA	9	GND
10	INT	10	ORXIN0-
11	RST	11	ORXIN0+
12	SS (GND)	12	ORXIN1-
		13	ORXIN1+
		14	ORXIN2-
		15	ORXIN2+
		16	ORXCLKIN-
		17	ORXCLKIN+
		18	ORXIN3-
		19	ORXIN3+
		20	ERXIN0-
		21	ERXIN0+
		22	ERXIN1-
		23	ERXIN1+
		24	ERXIN2-
		25	ERXIN2+
		26	ERXCLKIN-
		27	ERXCLKIN+
		28	ERXIN3-
		29	ERXIN3+
		30	GND
		31	FAULT
		32	RESET
		33	STBYB
		34	CSB
		35	SCL
		36	SDAI
		37	SDAO
		38	GND
		39	GND
		40	NC
		41	LEDA
		42	LEDA
		43	LEDA
		44	NC
		45	LEDK
		46	LEDK
		47	LEDK
		48	LEDK
		49	NTC_A
		50	NTC_K

TFT NOTE:
1.LCD TYPE:12.3",TFT,NORMALLY BLACK
2.VIEWING DIRECTION:ALL O'CLOCK
3.VLED=36.3Vmax,ILED=90mA*4,PLED=(13.1W)
4.BRIGHTNESS:850cd/m2 typ.730cd/m2min.WITH PCAP
,30000HR 50% BRIGHTNESS DECREASE
5.OPERATION TEMP.: -30°C~85°C
6.STORAGE TEMP.: -40°C~90°C
7.NON-MARK TOLERANCE is ±0.5mm
8.CONTRAST = 1000:1 typ.

PCAP Notes:
1. TP Structure: G+G;
2. TP Type: COF;IC: ILI2511
3. Transmittance: ≥86%
4. Hardless: >6H
5. Operating conditions: -10°C~+60°C, ≤85%RH
6. Storage conditions: -20°C~+70°C, ≤85%RH
7. Unspecified tolerance: ±0.2mm
8. All materials in the drawing comply with the Rosh

LED CIRCUIT



CONTROL DIMENTION:		XINSUN DISPLAY INTEGRATION	
REFERENCE DIMENTION:			
APPROVED:	JOBS	MODEL NAME	3rd ANGLE
CHECKED:	ERIC	PART NAME	UNIT
DESIGNED:	ISSCA	DRAWING NUMBER	SCALE
		METERIAL NUMBER	PAGE
			1/1