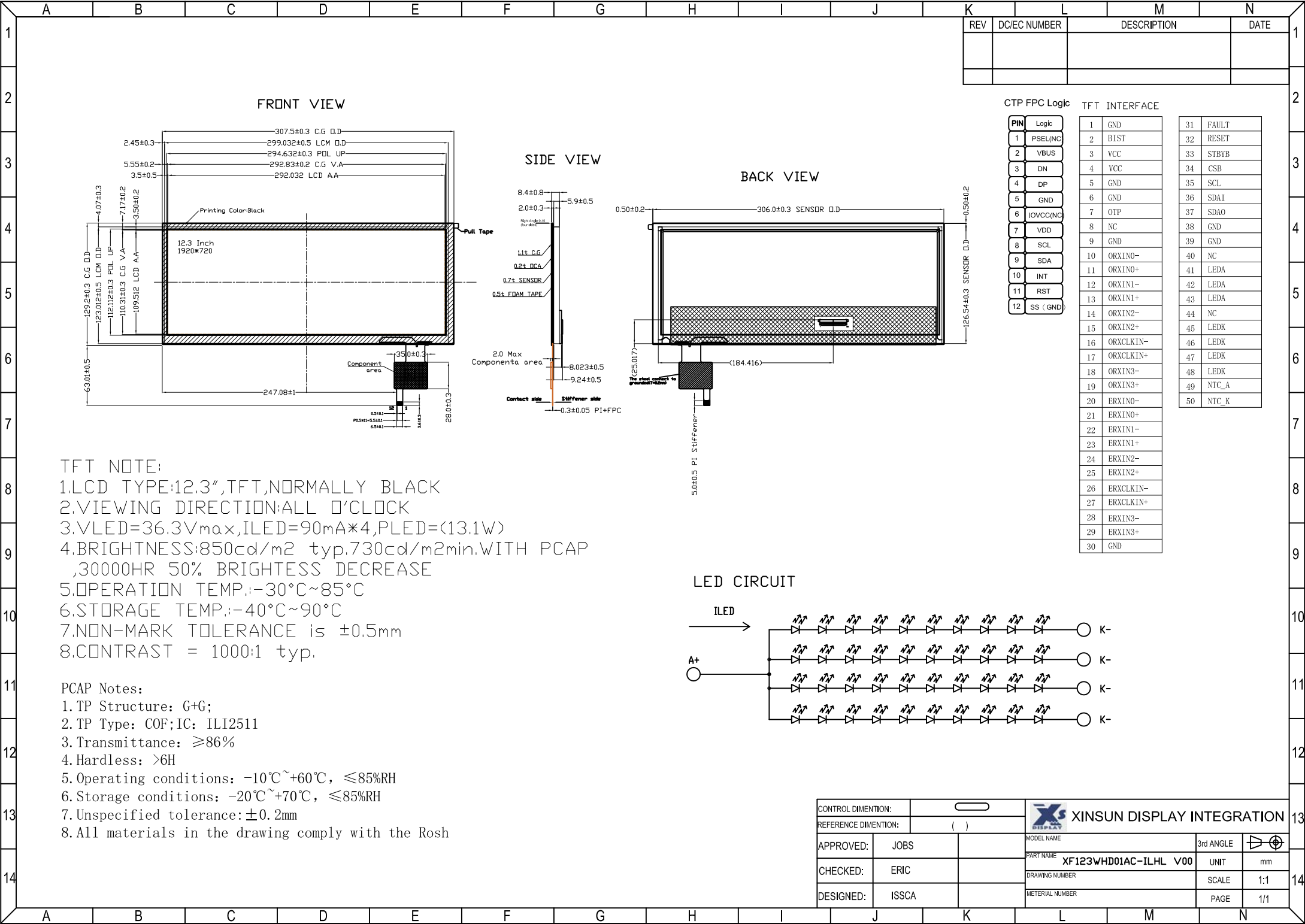




REV	DC/EC NUMBER	DESCRIPTION	DATE

CTP FPC Logic

PIN	Logic
1	PSEL(NC)
2	VBUS
3	DN
4	DP
5	GND
6	IOVCC(NC)
7	VDD
8	SCL
9	SDA
10	INT
11	RST
12	SS (GND)

TFT INTERFACE

1	GND	31	FAULT
2	BIST	32	RESET
3	VCC	33	STBYB
4	VCC	34	CSB
5	GND	35	SCL
6	GND	36	SDAI
7	OTP	37	SDAO
8	NC	38	GND
9	GND	39	GND
10	ORXIN0-	40	NC
11	ORXIN0+	41	LEDA
12	ORXIN1-	42	LEDA
13	ORXIN1+	43	LEDA
14	ORXIN2-	44	NC
15	ORXIN2+	45	LEDK
16	ORXCLKIN-	46	LEDK
17	ORXCLKIN+	47	LEDK
18	ORXIN3-	48	LEDK
19	ORXIN3+	49	NTC_A
20	ERXIN0-	50	NTC_K
21	ERXIN0+		
22	ERXIN1-		
23	ERXIN1+		
24	ERXIN2-		
25	ERXIN2+		
26	ERXCLKIN-		
27	ERXCLKIN+		
28	ERXIN3-		
29	ERXIN3+		
30	GND		

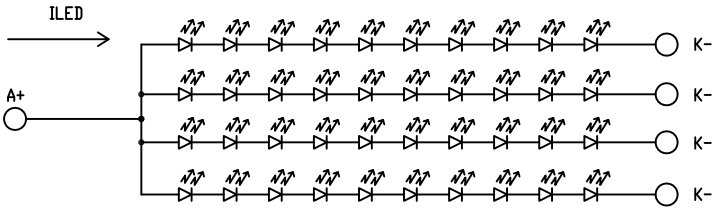
TFT NOTE:

- 1.LCD TYPE:12.3",TFT,NORMALLY BLACK
- 2.VIEWING DIRECTION:ALL O'CLOCK
- 3.VLED=36.3Vmax,ILED=90mA*4,PLED=(13.1W)
- 4.BRIGHTNESS:850cd/m2 typ.730cd/m2min.WITH PCAP
- 5.OPERATION TEMP.: -30°C~85°C
- 6.STORAGE TEMP.: -40°C~90°C
- 7.NON-MARK TOLERANCE is ±0.5mm
- 8.CONTRAST = 1000:1 typ.

PCAP Notes:

1. TP Structure: G+G;
2. TP Type: COF;IC: ILI2511
3. Transmittance: ≥86%
4. Hardless: >6H
5. Operating conditions: -10°C~+60°C, ≤85%RH
6. Storage conditions: -20°C~+70°C, ≤85%RH
7. Unspecified tolerance: ±0.2mm
8. All materials in the drawing comply with the Rosh

LED CIRCUIT



CONTROL DIMENTION:		XINSUN DISPLAY INTEGRATION	
REFERENCE DIMENTION:		()	
APPROVED:	JOBS	MODEL NAME	3rd ANGLE
CHECKED:	ERIC	PART NAME	UNIT
DESIGNED:	ISSCA	DRAWING NUMBER	SCALE
		MATERIAL NUMBER	PAGE