4. General Specifications

ADB-101WX04AH2 is a from HDMI signal to change the LVDS signal board.
This board is used in the 10.1 inch 1280*800 TFT.This product accords with ROHS.

4.1 Product Function Description

4.1.1 HDMI signal input
4.1.2 Power input DC +5.0V
4.1.3 No signal automatic shutdown

NO SIGNAL MODE:

SLEEP MODE:

4.2 Signal Input Standard

4.2.1 HDMI : (General 1.1 Version)

5. Operating and Storage Environment

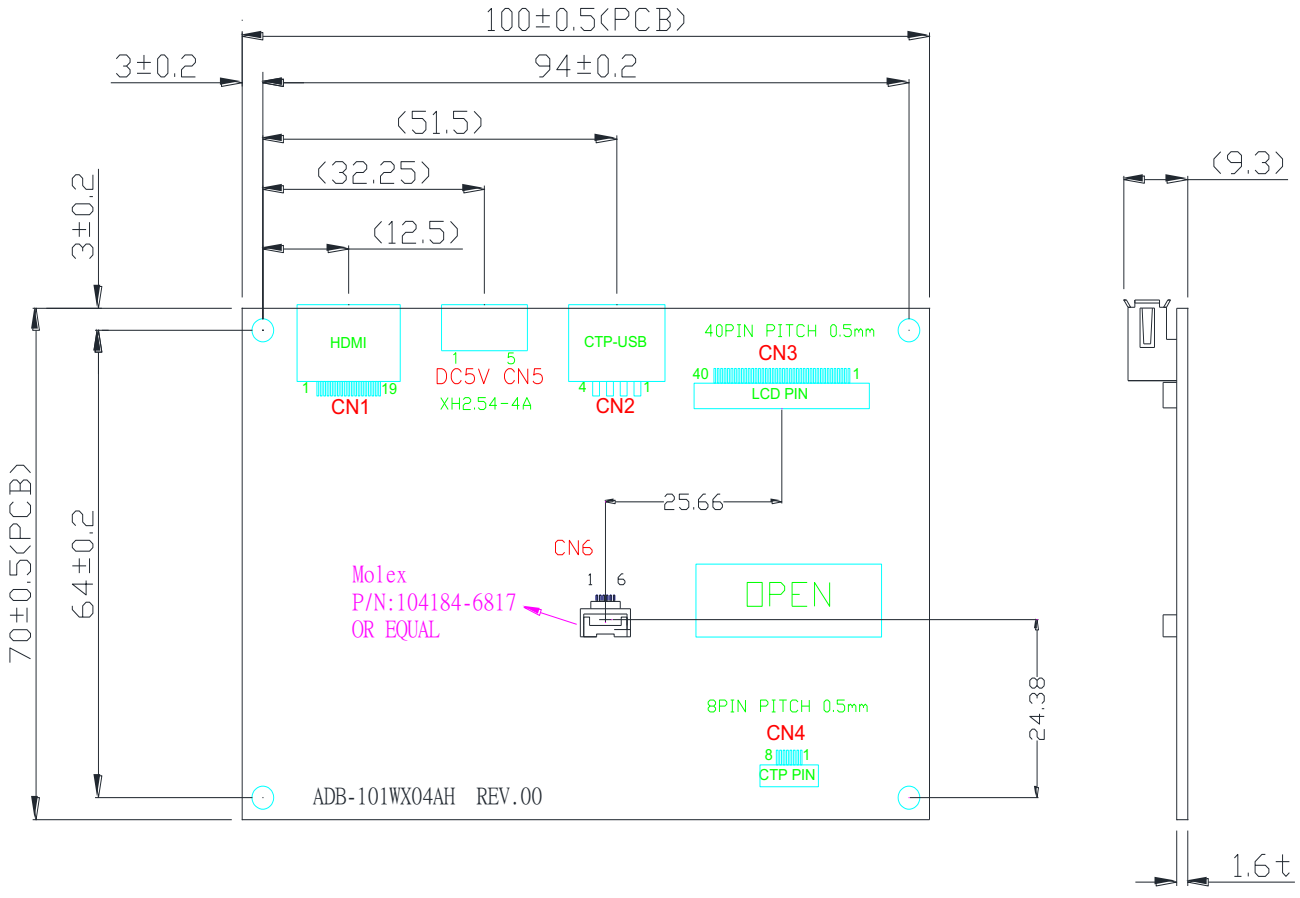
5.1 Operating Temperature：-20℃～+70℃
5.2 Operation Humidity：90%RH（no condensation）
5.3 Storage Temperature: -20℃～+70℃
5.4 Storage Humidity: 90%RH（no condensation）

6. Operating Power condition

(Ta=25℃)

Item	Min.	Typ.	Max.	Unit	Remark
Operating Voltage	-	+5.0	-	V	
Operating Current	-	300	-	mA	Without backlight driving
	-	800	-	mA	With backlight driving

7. Production outline drawing and physical Picture（unit:mm）



1.PCB Thickness:1.6t
2.PCB Material:FR4
3.UNIT:mm
4.UNMARKED TOLERANCE:±0.2

8. Product Pin definition

CN1（HDMI Input ）Interface

PIN NO.	Definition	Description
1	TMDS Data 2+	
2	TMDS Data Shield	
3	TMDS Data 2-	
4	TMDS Data 1+	
5	TMDS Data Shield	
6	TMDS Data 1-	
7	TMDS Data 0+	
8	TMDS Data Shield	
9	TMDS Data 0-	
10	TMDS Clock+	
11	TMDS Clock Shield	
12	TMDS Clock -	
13	CEC	
14	NC	
15	SCL	
16	SDL	
17	DDC/CEC GND	
18	+5V	
19	Hot Plug Detect	

CN5 Power Supply Input

PIN NO.	Definition	Description
1	+5.0V	POWER
2	+5.0V	POWER
3	GND	GROUND
4	GND	GROUND
5	PWM	Backlight pwm control

CN3(FPC Definition for LVDS of TFT）40PIN/0.5mm

PIN NO.	Symbol	I/O	Remark
1	NC	-	No connection
2	VDD	P	Power Supply
3	VDD	P	Power Supply
4	NC	-	No connection
5	NC	-	No connection
6	NC	-	No connection
7	GND	P	Ground
8	RXINO-	I	-LVDS Differential Data Input R0~R5,G0
9	RXINO+	I	+LVDS Differential Data Input R0~R5,G0
10	GND	P	Ground
11	RXIN1-	I	-LVDS Differential Data Input G1~G5,B0,B1
12	RXIN1+	I	+LVDS Differential Data Input G1~G5,B0,B1
13	GND	P	Ground
14	RXIN2-	I	-LVDS Differential Data Input B2~B5,HS,VS
15	RXIN2+	I	+LVDS Differential Data Input B2~B5,HS,VS
16	GND	P	Ground
17	RXCLKIN-	I	-LVDS Differential Clock Input LVDS CLK
18	RXCLKIN+	I	+LVDS Differential Clock Input LVDS CLK
19	GND	P	Ground
20	RXIN3-	I	-LVDS Differential Data Input R6,R7,G6,G7,B6,B7
21	RXIN3+	I	+LVDS Differential Data Input R6,R7,G6,G7,B6,B7
22	GND	P	Ground
23	NC	-	No connection
24	NC	-	No connection
25	GND	P	Ground
26	NC	-	No connection
27	NC	-	No connection
28	NC	-	No connection
29	NC	-	No connection
30	GND	P	Ground
31	LED-	P	Power Supply for LED Cathode
32	LED-	P	Power Supply for LED Cathode
33	NC	-	No connection
34	NC	-	No connection
35	NC	-	No connection

PIN NO.	Symbol	I/O	Remark
36	NC	-	No connection
37	NC	-	No connection
38	NC	-	No connection
39	LED+	P	Power Supply for LED Anode
40	LED+	P	Power Supply for LED Anode

CN2 USB for CTP Input

PIN NO.	Symbol	Function
1	VBUS	Power supply
2	DN	Data -
3	DP	Data+
4	GND	Ground

CN6 IIC for CTP

PIN NO.	Symbol	Function
1	VDD3.3	Power supply
2	WAKE	Reset
3	IIC INT	External Interrupt to the IC of CTP
4	IIC SCL	Serial clock
5	IIC SDA	Serial Inputloupt data bus
6	GND	Ground

9. Block Diagram